

RZ/A2M Group

R01AN4496EG0100

Rev.1.0

RZ/A2M STB Driver

Sept 18, 2018

Introduction

This application note describes the operation of the software STB Driver for the RZ/A2 device on the RZ/A2M CPU Board.

It provides a comprehensive overview of the driver. For further details please refer to the software driver itself.

The user is assumed to have knowledge of e² studio and to be equipped with an RZ/A2M CPU Board.

Target Device

RZ/A2M Group

Driver Dependencies

This driver depends on:

- Drivers
 - o STDIO

Referenced Documents

Document Type	Document Name	Document No.
User's Manual	RZ/A2M Hardware Manual	R01UH0746EJ

List of Abbreviations and Acronyms

Abbreviation	Full Form
ANSI	American National Standards Institute
API	Application Programming Interface
ARM	Advanced RISC Machines
CPU	Central Processing Unit
HLD	High Layer Driver
IDE	Integrated Development Environment
LLD	Low Layer Driver
OS	Operating System
RAM	Random Access Memory
STDIO	Standard Input/Output

Table 1-1 List of Abbreviations and Acronyms

Contents

1. Outline of Software Driver	3
2. Description of the Software Driver	4
2.1 Structure	4
2.2 Description of each file	5
2.3 Low Layer Driver	6
3. Example of Use	7
3.1 Start Module	7
3.2 Stop Module	7
3.3 Software Reset	7
3.4 Get Version	7
4. OS Support	8
5. How to Import the Driver	9
5.1 e ² studio	9
Website and Support	10

1. Outline of Software Driver

The STB (Standby) driver controls the various power down states and power saving features of the MPU. This includes turning unused peripherals on and off in order to reduce power consumption.

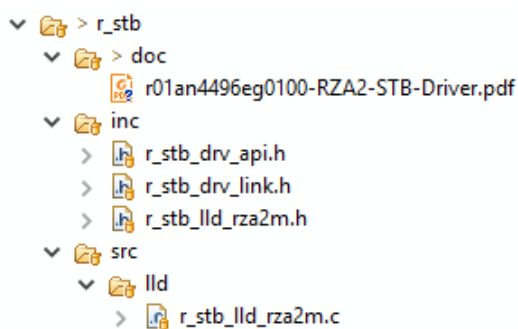
2. Description of the Software Driver

The key features of the driver include:

- The ability to put a peripheral in a low power mode (module stop)
- The ability to take a peripheral out of low power mode (module start)
- Issuing a software reset command to a module
- Putting the device into a low power mode
- Enable/disable of on-chip data-retention RAM
- System software reset

2.1 Structure

Unlike many of the other drivers, the STB driver currently consists of a single layer: the Low Layer Driver (LLD). This includes all the hardware specific functions and provides the API to the application.



2.2 Description of each file

Each file's description can be seen in the following table.

Filename	Usage	Description
Application-Facing Driver API		
r_stb_drv_api.h	Application	** FOR FUTURE USE **
Low Layer API		
r_stb_lld_xxxx.h	Private (HLD/LLD only)	Low Layer Driver (LLD) header file (where "xxxx" is a device and board-specific identification). This is the header file to include in application code.
Abstraction Link between High and Low Layer Drivers (HLD/LLD Link)		
r_stb_drv_link.h	Private (HLD/LLD only)	** FOR FUTURE USE **
Low Layer Driver (LLD) Source		
r_stb_lld_xxxx.c	Private (LLD only)	(Where "xxxx" is a device and board specific identification). Provides the definitions for the Low Layer Driver interface.

2.3 Low Layer Driver

The Low Layer Driver provides the functions to configure the hardware.

Return Type	Function	Description	Arguments	Return
e_stb_err_t	R_STB_RequestModuleStart (e_stb_module_t module)	Prepare to start module	module: [in] module number	STB_SUCCESS , STB_INVALID_MODULE , STB_INVALID_STATE or STB_AGAIN
e_stb_err_t	R_STB_RequestModuleStop (e_stb_module_t module)	Prepare to stop module	module: [in] module number	STB_SUCCESS , STB_INVALID_MODULE , STB_INVALID_STATE or STB_AGAIN
e_stb_err_t	R_STB_StopModule (e_stb_module_t module)	Stop module	module: [in] module number	STB_SUCCESS or STB_INVALID_MODULE
e_stb_err_t	R_STB_StartModule (e_stb_module_t module)	Start module	module: [in] module number	STB_SUCCESS or STB_INVALID_MODULE
e_stb_err_t	R_STB_SetModuleResetState (e_stb_module_t module, e_stb_reset_state_t state)	Set module reset state	module: [in] module number state [in]: reset state	STB_SUCCESS , STB_INVALID_MODULE , or STB_INVALID_STATE
e_stb_err_t	R_STB_GetModuleResetState (e_stb_module_t module, e_stb_reset_state_t *p_state)	Get module reset state	module: [in] module number p_state [out]: the current reset state	STB_SUCCESS , STB_INVALID_MODULE , or STB_INVALID_STATE
uint32_t	R_STB_GetVersion (st_drv_info_t *pinfo)	Get Low Layer Driver version information	pinfo: [out] pointer to version information structure	DRV_SUCCESS

3. Example of Use

This section gives simple examples for starting a module, stopping a module, performing a module software reset, and finally getting the driver version.

3.1 Start Module

```
e_stb_err_t result;  
e_stb_module_t module;  
  
module = MODULE_JCU;  
  
result = R_STB_StartModule(module);
```

3.2 Stop Module

```
result = R_STB_StopModule(module);
```

3.3 Software Reset

```
e_stb_reset_state_t state;  
  
state = MODULE_RESET_STATE;  
  
result = R_STB_SetModuleResetState(module, state);
```

3.4 Get Version

```
st_ver_info_t info;  
uint32_t get_version_result;  
  
get_version_result = R_STB_GetVersion(&info);
```


4. OS Support

This driver supports any OS through using the OS abstraction module. For more details about the abstraction module please refer to the OS abstraction module application note.

5. How to Import the Driver

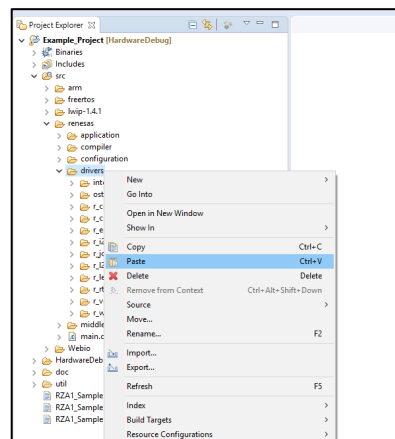
This section describes how to import the driver into your project. Generally, there are two steps in any IDE:

- 1) Copy the software driver to the location in the source tree that you require for your project.
- 2) Add the include path of the driver to the compiler.

5.1 e² studio

To import the driver into your project please follow the instructions below.

- 1) In Windows Explorer, right-click on the `r_stb` folder, and click **Copy**.
- 2) In e² studio Project Explorer view, select the folder where you wish the driver project to be located; right-click and click **Paste**.
- 3) Right-click on the parent project folder (in this case 'Example_Project') and click **Properties ...**
- 4) In 'C/C++ Build → Settings → Cross ARM Compiler → Includes', add the include folder of the newly added driver, e.g. `'${ProjDirPath}\src\renesas\drivers\r_stb\inc'`



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Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Sept 18, 2018	All	Created document.

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Handling of Unused Pins

Handle unused pins in accordance with the directions given under Handling of Unused Pins in the manual.

- ¾ The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- ¾ The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

- ¾ The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

- ¾ When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

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Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.

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